

# Agilent 4294A Precision Impedance Analyzer Data Sheet

#### **Specifications**

Specifications describe the instrument's warranted performance over the temperature range of 0 °C to 40 °C (except as noted). Supplemental performance characteristics are intended to provide information that is useful in applying the instrument by given non-warranted performance parameters. These are denoted as SPC (supplemental performance characteristics), typical, or nominal. Warm-up time must be greater than or equal to 30 minutes after power on for all specifications.

#### **Basic Characteristics**

#### **Measurement Parameter**

Impedance Parameters	Ζ -θ, R-X, Ls-Rs, Ls-Q, Cs-Rs, Cs-Q, Cs-D,  Υ -θ, G-B, Lp-G, Lp-Q, Cp-G, Cp-Q, Cp-D, Complex Z-Y,  Ζ -Ls,  Ζ -Cs,  Ζ -Lp,
	Z -Cp,  Z -Rs,  Z -Q,  Z -D, Lp-Rp, Cp-Rp

#### **Measurement Terminal**

Configuration	Four-terminal pair configuration
Connector type	Four BNC (female) connectors. Can be converted to one port ter- minal using the Agilent 42942A Terminal Adapter (7-mm port) or 42941A Impedance Probe (3.5-mm port).

#### **Source Characteristics**

Frequency	
Range 40 Hz to 110 MHz	
Resolution	1 mHz



#### Accuracy

without Option 4294A-1D5	±20 ppm (at 23 ±5 °C) ±40 ppm (at 0 to 55 °C)	
with Option 4294A-1D5	±0.13 ppm (at 0 to 55 °C)	
Voltage Signal Level		
Range	5 mVrms to 1 Vrms	
Resolution	1 mV	
Accuracy		
at four-terminal pair port of the 4294A or 7-mm port of the 42942A	$\pm [(10 + 0.05 \text{ X } f)\% + 1 \text{ mV}] (\text{at } 23 \pm 5 \text{ °C})$	

at measurement port of the 42941A, 16048G/H	±[(15 + 0.1 X <i>f</i> )% + 1 mV] (at 23 ±5 °C)	

#### NOTE

*f*: frequency [MHz].

These characteristics apply when OPEN is connected to each port. Test signal level should be  $\leq 0.5$  Vrms when the measured impedance is  $\leq 50 \Omega$ . Beyond 23 ±5 °C of temperature, test signal level setting accuracy is twice as bad as described.

#### **Current Signal Level**

Range	200 µArms to 20 mArms
Resolution	20 μΑ
Accuracy	
at four-terminal pair port of the 4294A	
at ≤15 MHz	+[10% + 50 μA], –[(10 + 0.2 X <i>f</i> ²)% + 50 μA] (at 23 ±5 °C, typical)
at >15 MHz	±[(10 + 0.3 X f)% + 50 μA] (at 23 ±5 °C, typical)
at 7-mm port of the 42942A	
at ≤5 MHz	+[10% + 50 μA], –[(10 + 1 X f²)% + 50 μA] (at 23 ± 5 °C, typical)
at >5 MHz	±[(10 + 0.3 X f)% + 50 μA] (at 23 ±5 °C, typical)

at measurement port of the 42941A, 16048G/H	
at ≤5 MHz	+[10% + 50 μA], –[(15 + 1.5 X f²)% + 50 μA] (at 23 ±5 °C, typical)
at >5 MHz	±[(20 + 0.3 X <i>f</i> )% + 50 μA] (at 23 ±5 °C, typical)

#### NOTE

*f*: frequency [MHz].

These characteristics apply when SHORT is connected to each port. Test signal level should be  $\leq 20$  mArms when the measured impedance is  $\leq 50 \Omega$ .

#### **Signal Level Monitor**

Voltage range	(Same as the voltage signal level setting range)
Voltage monitor accuracy	
at four-terminal pair port of the 4294A or 7-mm port of the 42942A	±(10 + 0.05 X f + 100/Z <sub>X</sub> )[%] (at 23 ±5 °C, typical)
at measurement port of the 42941A, 16048G/H	±(10 + 0.15 X f + 100/Z <sub>X</sub> )[%] (at 23 ±5 °C, typical)
Current range	(Same as the current signal level setting range)
Current monitor accuracy	
at four-terminal pair port of the 4294A or 7-mm port of the 42942A	$\pm (10 + 0.3 \times f + Z_X / 100)[\%]$ (at 23 ±5 °C, typical)
at measurement port of the 42941A, 16048G/H	$\pm (10 + 0.4 \times f + Z_X/100)[\%]$ (at 23 ±5 °C, typical)

#### NOTE

*f*: frequency [MHz],  $Z_X$  impedance measurement value [ $\Omega$ ]. Beyond 23 ±5 °C, the test signal level monitor accuracy is twice as bad as described.

#### **Output Impedance**

Output impedance

25  $\Omega$  (nominal)

### **DC Bias Function**

DC voltage bias	
Range	0 to $\pm$ 40 V (see Figure 1)
Resolution	1 mV
Accuracy	$\pm [0.1\% + (5 + 30 \times  I_{mon} ) \text{ mV}] (\text{at } 23 \pm 5 \text{ °C}) \\\pm [0.2\% + (10 + 30 \times  I_{mon} ) \text{ mV}] (\text{beyond } 23 \pm 5 \text{ °C})$
OC current bias	
Range	0 to $\pm 100$ mA (see Figure 1)
Resolution	40 µA
Accuracy	$\pm [2\% + (0.2 +  V_{mon} /20) \text{ mA}] (\text{at } 23 \pm 5 ^{\circ}\text{C}) \\ \pm [4\% + (0.4 +  V_{mon} /20) \text{ mA}] (\text{beyond } 23 \pm 5 ^{\circ}\text{C})$
DC voltage bias at constant voltage mode	
Range	0 to $\pm$ 40 V (see Figure 1)
Resolution	1 mV
Accuracy	$\begin{array}{l} \pm [0.5\% + (5 + Z_d \ X \   I_{mon}   ) \ mV] \ (at \ 23 \ \pm 5 \ ^\circ C, \ typical) \\ \pm [1.0\% + (10 + Z_d \ X \   I_{mon}   ) \ mV] \ (beyond \ 23 \ \pm 5 \ ^\circ C, \ typical) \end{array}$
OC current bias at constant curre	nt mode
Range	0 to ±100 mA (see Figure 1)
	o to = 100 mm (000 mgato 1)
Resolution	40 μA
Resolution Accuracy	40 μA ±[1% + (0.5 +  V <sub>mon</sub>  /10000) mA] (at 23 ±5 °C, typical)
Accuracy	40 μΑ
Accuracy	40 μA ±[1% + (0.5 +  V <sub>mon</sub>  /10000) mA] (at 23 ±5 °C, typical)
Accuracy DC bias monitor	40 $\mu$ A $\pm [1\% + (0.5 +  V_{mon} /10000) \text{ mA}] (at 23 \pm 5 °C, typical)$ $\pm [2\% + (1.0 +  V_{mon} /5000) \text{ mA}] (beyond 23 \pm 5 °C, typical)$

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DC current monitor accuracy	$\pm [1\% + (0.5 +  V_{mon} /10000)mA] (at 23 \pm 5 °C)$ $\pm [2\% + (1.0 +  V_{mon} /5000)mA] (beyond 23 \pm 5 °C)$
Output impedance	25 Ω (nominal)

#### NOTE

- $V_{mon}$ : dc voltage bias monitor reading value [mV]
- *I<sub>mon</sub>* : dc current bias monitor reading value [mA]
- $Z_d = 0.3$  (at four-terminal pair port of the 4294A, adapter setup: NONE)
- $Z_d = 2.0$  (at 3.5 mm port of the 42941A, adapter setup: 42941A Impedance Probe)
- $Z_d = 0.5$  (at 7-mm port of the 42942A, adapter setup: 42942A Terminal Adapter)
- $Z_d = 1.0$  (at measurement port of the 16048G, adapter setup: four-terminal pair 1 m)
- $Z_d = 1.5$  (at measurement port of the 16048H, adapter setup: four-terminal pair 2 m)

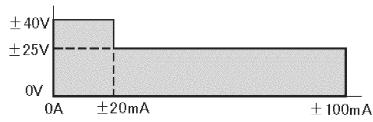


Figure 1. DC Bias Range (SPC)

#### **Sweep Characteristics**

Available sweep parameters	Frequency, Signal voltage, Signal current, DC bias voltage, DC bias current
Sweep type	Linear, Log <sup>1</sup> , List <sup>1</sup> , Zero-span, Manual
Manual sweep	Available for all sweep types
Sweep direction	Up sweep, Down sweep
Number of measurement points	2 to 801 points
List Sweep	
Available setup parameters for each segment	Sweep frequency range, Number of measurement points, Signal level (voltage or current), DC bias (voltage or current), Measure- ment bandwidth, Point averaging factor

1. Frequency sweep only.

Number of segments	1 to 18
Sweep span type	Segment span or single span
Delay time	
Туре	Point delay or sweep delay
Range	0 sec to 30 sec
Resolution	1 msec

#### **Measurement Time**

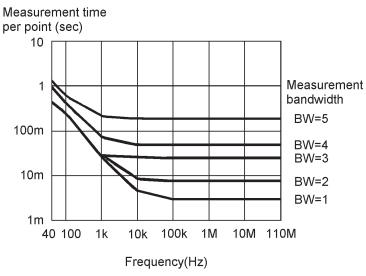


Figure 2. Measurement Time (SPC)

#### NOTE

When the Agilent 42941A Impedance Probe or 42942A Terminal Adapter is used, measurement time is 1.5 times longer than the value in Figure 2.

### **Trigger Function**

Trigger type	Continuous, Single, Number of groups
Trigger source	Internal (Free run), External (BNC connector input), GPIB or LAN, Manual (Front key)
Trigger event type	Point trigger, Sweep trigger

#### Measurement Bandwidth/Averaging

Measurement	bandwidth

Range	1 (Fast) to 5 (Precise), 5 steps	
Averaging		
Туре	Sweep-to-sweep averaging, Point averaging	
Averaging factor	1 to 256 (integer)	

#### Adapter Setup

Adapter Selection	
NONE	No adapter (the 16047E, etc. direct connection type test fixture is connected)
4TP 1M	Four-terminal pair 1 m (16048G)
4TP 2M	Four-terminal pair 2 m (16048H)
7-mm 42942A	Terminal Adapter (42942A)
PROBE 42941A	Impedance Probe (42941A)

#### Calibration

Calibration	
User calibration	Calibration performed with user-defined calibration kit (OPEN, SHORT, LOAD)
Port extension	Compensation performed when the measurement terminal is expanded from the 7-mm connector of the 42942A Terminal Adapter or the 3.5-mm connector of the 42941A Impedance Probe. Enter electrical length or delay time for the extension.
Fixture compensation	Compensation performed at the device contacts of the test fix- ture using OPEN, SHORT, LOAD.
Calibration points	Fixed points, or User points determined by sweep setups

#### **Measurement Accuracy**

#### **Conditions of Accuracy Specifications**

Temperature	
Four-terminal pair port of the 4294A's front panel	23 $\pm$ 5 °C Beyond 23 $\pm$ 5 °C, the measurement accuracy is twice as bad as described.
7-mm port of the 42942A Terminal Adapter	Within ±5 °C from the adapter setup temperature. Measurement accuracy applies when the adapter setup is per- formed at 23 ±5 °C. When the adapter setup is performed beyond 23 ±5 °C, the measurement accuracy is twice as bad as described.
3.5-mm port of the 42941A Impedance Probe	Within ±5 °C from the adapter setup temperature. Measurement accuracy applies when the adapter setup is per- formed at 23 ±5 °C. When the adapter setup is performed beyond 23 ±5 °C, the measurement accuracy is twice as bad as described.
Measurement terminal of the 16048G or 16048H	Within ±5 °C from the adapter setup temperature. Measurement accuracy applies when the adapter setup is per- formed at 23 ±5 °C. When the adapter setup is performed beyond 23 ±5 °C, the measurement accuracy is twice as bad as described
Measurement bandwidth	5
Measurement Accuracy	
Z ,  Y  accuracy	± <i>E</i> [%] (see Equation 1 on page 10, Equation 2 on page 12, Equation 3 on page 14)
θ ассигасу	±E /100 [rad]
L, C, X, B accuracy	
at $D_x \leq 0.1$	± <i>E</i> [%]
at <i>D<sub>x</sub></i> >0.1	$\pm E \times \sqrt{1 + D_x^2} [\%]$
R accuracy	

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at $D_x \le 0.1 \ (Q_x \ge 10)$	$Rp: \pm \frac{E}{D_x \mp E/100} [\%]$
	Rs: ±E/D <sub>x</sub> [%]
at $0.1 < D_x < 10$ ( $0.1 < Q_x < 10$ )	$Rp: \pm E \times \frac{\sqrt{1 + D_x^2}}{D_x \mp \frac{E}{100} \times \sqrt{1 + D_x^2}} [\%]$
	Rs: $\pm E \times \frac{\sqrt{1 + D_x^2}}{D_x}$ [%]
at $D_x \ge 10 \ (Q_x \le 0.1)$	±E [%]
D accuracy	
at <i>D<sub>x</sub></i> ≤0.1	±E/100
at 0.1 < <i>D</i> <sub>x</sub> ≤1	±E X (1 + D <sub>x</sub> )/100
$\Omega$ accuracy (at $Q_x \times D_a < 1$ )	
at $Q_x \le 10 \ (D_x \ge 0.1)$	$\pm \frac{\Omega_x^2 X E(1 + D_x)/100}{1 \mp \Omega_x X E(1 + D_x)/100}$
at <i>Q<sub>x</sub></i> >10 ( <i>D<sub>x</sub></i> <0.1)	$\pm \frac{Q_x^2 X E/100}{1 \mp Q_x X E/100}$
G accuracy	
at <i>D<sub>x</sub></i> >0.1	$\pm E \times \frac{\sqrt{1 + D_x^2}}{D_x} [\%]$
at $D_x \leq 0.1$	±E/D <sub>x</sub> [%]

#### NOTE

 $D_x$ : measurement value of D.

 $Q_x$ : measurement value of Q.  $D_a$ : measurement accuracy of D.

#### Impedance Measurement Accuracy at Four-Terminal Pair Port

Equation 1 shows the impedance measurement accuracy [%] at four-terminal pair port of the Agilent 4294A, or measurement port of the 16048G/16048H.

Equation 1. Impedance Measurement Accuracy [%] at Four-Terminal Pair Port

$$\mathsf{E} = \mathsf{E}_{\mathsf{p}}' + \left(\frac{\mathsf{Z}_{\mathsf{s}}'}{|\mathsf{Z}_{\mathsf{x}}|} + \mathsf{Y}_{\mathsf{o}}' \cdot |\mathsf{Z}_{\mathsf{x}}|\right) \times 100$$

Where,

$$\begin{split} & E_{\mathsf{P}}' = E_{\mathsf{PL}} + E_{\mathsf{PBW}} + E_{\mathsf{POSC}} + E_{\mathsf{P}} [\%] \\ & Y_0' = Y_{\mathsf{OL}} + K_{\mathsf{BW}} \, X \, \mathsf{Ky}_{\mathsf{OSC}} \, X \, (\mathsf{Y}_{\mathsf{ODC}} + Y_0) \, [\mathsf{S}] \\ & Z_\mathsf{S}' = Z_{\mathsf{SL}} + K_{\mathsf{BW}} \, X \, \mathsf{Kz}_{\mathsf{OSC}} \, X \, \mathsf{Z}_\mathsf{S} \, [\Omega] \end{split}$$

 $Y_{o}$ ,  $E_p$ ,  $Z_s$ : See Figure 3 on page 17.

$E_{posc} [\%] =$	
at oscillator level >500 mV	$0.03 \times \left( \frac{1000}{V_{mV}} - 1 \right) + \frac{f}{100}$
at oscillator level >250 mV, ≤500 mV	$0.03 \times \left(\frac{500}{V_{mV}} - 1\right)$
at oscillator level >125 mV, ≤250 mV	$0.03 \times \left(\frac{250}{V_{mV}} - 1\right)$
at oscillator level >64 mV, ≤125 mV	$0.03  imes \left( rac{125}{V_{mV}} - 1  ight)$
at oscillator level ≤64 mV	$\left(rac{64}{V_{mV}}-1 ight) imes (0.03+E_{PBW})$
Ky <sub>osc</sub> =	
at oscillator level >500 mV	1000 V <sub>mV</sub>
at oscillator level ≤500 mV	500 V <sub>mV</sub>
Kz <sub>osc</sub> =	
at oscillator level >500 mV	2
at oscillator level >250 mV, ≤500 mV	500 V <sub>mV</sub>
at oscillator level >125 mV, ≤250 mV	250 V <sub>mV</sub>
at oscillator level >64 mV, ≤125 mV	$\frac{125}{V_{mV}}$
at oscillator level ≤64 mV	$\frac{64}{V_{mV}}$

$F_{PBW}[\%] =$	
at measurement BW = 5	0
at measurement BW = 4	
frequency ≥50 kHz	0.03
frequency <50 kHz	0.06
at measurement BW = 3	
frequency ≥50 kHz	0.1
frequency <50 kHz	0.2
at measurement BW = 2	
frequency ≥50 kHz	0.2
frequency <50 kHz	0.4
at measurement BW = 1	
frequency ≥50 kHz	0.4
frequency <50 kHz	0.8
<sub>BW</sub> =	
at measurement BW = 5	1
at measurement BW = 4	1
at measurement BW = 3	
frequency ≤1 MHz	3
frequency >1 MHz	4
at measurement BW = 2	
frequency ≤1 MHz	4
frequency >1 MHz	5
at measurement BW = 1	
frequency ≤1 MHz	6
frequency >1 MHz	10
odc =	
at dc bias range = 1 mA	0 [S]
at dc bias range = 10 mA	1 [µ\$]
at dc bias range = 100 mA	10 [µS]

### $E_{PBW}[\%] =$

E <sub>PL</sub>	[%]	=
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when 16048G is used	$0.02 + 2 X \frac{f}{100}$
when 16048H is used	$0.02 + 3 \times \frac{f}{100}$
$\overline{Y_{OL}} =$	
when 16048G is used	500 X <u>f</u> [nS]
when 16048H is used	1 Χ <sub>1</sub> <sup>f</sup> / <sub>100</sub> [μS]
$\overline{Z_{SL}} =$	
when 16048G or 16048H is used	
frequency ≥500 Hz	2 [mΩ]
frequency <500 Hz	5 [mΩ]

NOTE SPC at frequency >10 MHz. f: frequency in MHz.  $V_{mV}$ : oscillator level in mV

#### Impedance Measurement Accuracy at 7-mm Port of the Agilent 42942A

Equation 2 shows the impedance measurement accuracy [%] at 7-mm port of the 42942A Terminal Adapter.

Equation 2. Impedance Measurement Accuracy [%] at 7-mm Port of the Agilent 42942A

$$\mathsf{E} = \mathsf{E}_{\mathsf{p}}' + \left(\frac{\mathsf{Z}_{\mathsf{s}}'}{|\mathsf{Z}_{\mathsf{X}}|} + \mathsf{Y}_{\mathsf{o}}' \cdot |\mathsf{Z}_{\mathsf{X}}|\right) \times 100$$

Where,

$$\begin{split} & \mathsf{E}_{\mathsf{P}}' = \mathsf{E}_{\mathsf{PBW}} + \mathsf{E}_{\mathsf{POSC}} + \mathsf{E}_{\mathsf{P}} [\%] \\ & \mathsf{Y}_0' = \mathsf{K}_{\mathsf{BW}} \; \mathsf{X} \; \mathsf{K}_{\mathsf{YOSC}} \; \mathsf{X} \; (\mathsf{Y}_{\mathsf{ODC}} + \mathsf{Y}_0) \; [\mathsf{S}] \\ & \mathsf{Z}_{\mathsf{S}}' = \mathsf{K}_{\mathsf{BW}} \; \mathsf{X} \; \mathsf{K}_{\mathsf{ZOSC}} \; \mathsf{X} \; \mathsf{Z}_{\mathsf{S}} \; [\Omega] \end{split}$$

 $Y_{o}$ ,  $E_{p}$ ,  $Z_{s}$ : See Figure 3 on page 17.

 $E_{posc} [\%] =$ 

at oscillator level >500 mV  $\frac{f}{100} \times \left(\frac{V_{mV}}{500} - 1\right)$ 

0

at oscillator level >125 mV, ≤500 mV

at oscillator level ≤125 mV	$\left(rac{125}{V_{mV}}-1 ight) imes$ (0.05 + E <sub>PBW</sub> )
Ky <sub>osc</sub> =	
at oscillator level ≥500 mV	1
at oscillator level <500 mV	500 V <sub>mV</sub>
Kz <sub>osc</sub> =	
at oscillator level >500 mV	$2 + \frac{f}{100}$
at oscillator level >250 mV, ≤500 mV	500 V <sub>mV</sub>
at oscillator level ≤250 mV, >125 mV	250 V <sub>mV</sub>
at oscillator level ≤125 mV	125 V <sub>mV</sub>
$E_{PBW}[\%] =$	
at measurement BW = 5	0
at measurement BW = 4	
frequency ≥50 kHz	0.03
frequency <50 kHz	0.06
at measurement BW = 3	
frequency ≥50 kHz	0.1
frequency <50 kHz	0.2
at measurement BW = 2	
frequency ≥50 kHz	0.2
frequency <50 kHz	0.4
at measurement BW = 1	
frequency ≥50 kHz	0.4
frequency <50 kHz	0.8
K <sub>BW</sub> =	
at measurement BW = 5	1
at measurement BW = 4	1

at measurement BW = 3	3
at measurement BW = 2	4
at measurement BW = 1	6
Y <sub>ODC</sub> =	
at dc bias range = 1 mA	0 [S]
at dc bias range = 10 mA	1 [µ\$]
at dc bias range = 100 mA	10 [µS]

### NOTE

f: frequency in MHz.  $V_{mV}$ : oscillator level in mV.

#### Impedance Measurement Accuracy at 3.5-mm Port of the Agilent 42941A

Equation 3 shows the impedance measurement accuracy [%] at 3.5-mm port of the 42941A Impedance Probe.

#### Equation 3. Impedance Measurement Accuracy [%] at 3.5-mm Port of the Agilent 42941A

$$\mathsf{E} = \mathsf{E}_{\mathsf{p}}' + \left(\frac{\mathsf{Z}_{\mathsf{s}}'}{|\mathsf{Z}_{\mathsf{X}}|} + \mathsf{Y}_{\mathsf{o}}' \cdot |\mathsf{Z}_{\mathsf{X}}|\right) \times 100$$

Where,

 $Y_{o}$ ,  $E_p$ ,  $Z_s$ : See Figure 3 on page 17.

### $E_{posc} \, [\%] =$

$\frac{f}{100} \times \left(\frac{V_{mV}}{500} - 1\right)$
0
$\left(\frac{125}{V_{mV}}-1 ight) imes$ (0.05 + E <sub>PBW</sub> )
1
500 V <sub>mV</sub>

$2 + \frac{f}{100}$ <u>500</u>
V <sub>mV</sub>
250 V <sub>mV</sub>
125 V <sub>mV</sub>
0
0.03
0.06
0.1
0.2
0.2
0.4
0.4
0.8
1
1
3
4
6
0 [S]
1 [µS]
10 [µS]

#### $Kz_{osc} =$

#### NOTE

f: frequency in MHz.  $V_{mV}$ : oscillator level in mV.

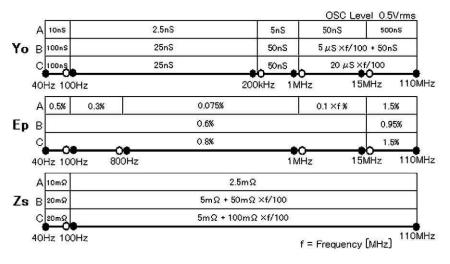
#### Temperature Coefficient of the Agilent 42941A Impedance Probe (SPC)

Proportional part (at 50 $\Omega$ measurement)	
Z  deviation [ppm/°C]	
at frequency ≤1 MHz	<5
at frequency >1 MHz	$20 + 500 \text{ X}  \frac{\text{f}}{100}$
θ deviation [µrad/°C]	
at frequency ≤1 MHz	<5
at frequency >1 MHz, ≤5 MHz	$30 \times \frac{f}{5}$
at frequency >5 MHz, ≤30 MHz	$50 + 150 X \frac{f}{30}$
at frequency >30 MHz	200
Residual part	
Residual impedance	5 Χ <mark>f</mark> [mΩ/°C]
Residual admittance	<u>f</u> [μS/°C]

#### NOTE

f: frequency in MHz.

These characteristics apply when the temperature of the probe (tip to 30 cm) is changed.



A = 4294A front panel 4 terminal pair port (no extension),

B = 7 -mm one port (with 42942A).C = Probe 3.5-mm port (with 42941A).

For accuracy at probe tip, add the following error factors (typical): Yo:  $+ 2\pi f \times 0.1 \ \mu S$ Zs: + 20mΩ

Figure 3. Parameters  $Y_0$ ,  $E_p$ , and  $Z_S$ 

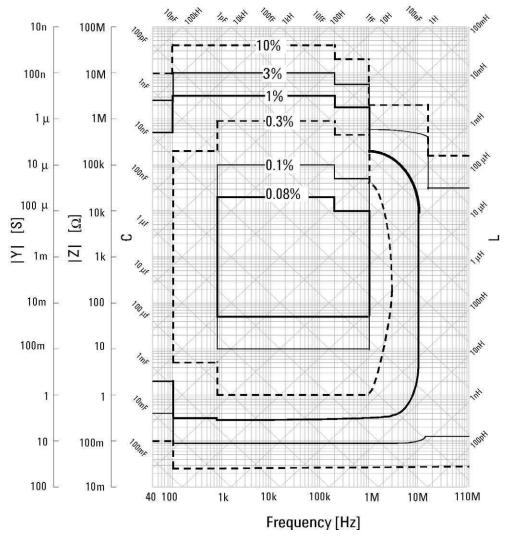


Figure 4. Examples of Calculated Impedance Measurement Accuracy at Four-Terminal Pair Port of the Agilent 4294A's Front Panel (Oscillator Level = 0.5 Vrms)

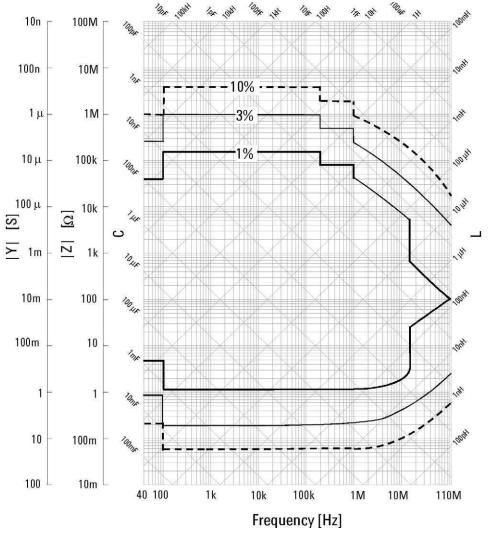


Figure 5. Impedance Measurement Accuracy at 7-mm Port of the Agilent 42942A Terminal Adapter Connected to the Agilent 4294A (Oscillator Level = 0.5 Vrms)

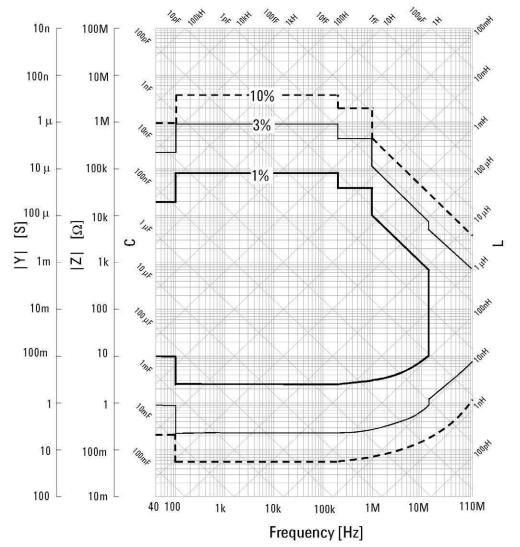


Figure 6. Impedance Measurement Accuracy at 3.5-mm port of the Agilent 42941A Impedance Probe Connected to the Agilent 4294A (Oscillator Level = 0.5 Vrms)

#### **Display Function**

Display	
Size/Type	8.4 inch color LCD (TFT)
Number or pixels	640 X 480 (VGA)
Scale type	
X axis scale	Linear and Log
Y axis scale	Linear and Log (depends on the sweep type)
Number of traces	
Data trace	2 traces (trace A and trace B)
Memory trace	2 traces (trace A and trace B)
Split display	Available (trace A: upper half, trace B: lower half)
Instrument/IBASIC display selection	All Instrument, Half Instrument and half IBASIC, all IBASIC, or Instrument and IBASIC status.
Other display function	Inactive trace off, Trace accumulation, Phase expansion
Data math function	Data—Memory, Data/Memory <sup>1</sup> , Delta% <sup>2</sup> , Offset
<ol> <li>Complex Z-Y measurement only.</li> <li>Except for Complex Z-Y measurement.</li> </ol>	

#### **Marker Function**

Marker type and number	
Main marker	One for each trace (A and B).
Sub marker	Seven for each trace (A and B).
$\Delta$ marker	One for each trace (A and B).
Marker search	
Search type	Maximum, Minimum, Target, Peak, Trace bandwidth analysis
Search track	Performs search by each sweep
Marker X-axis display	Sweep parameter value, Sweep elapsed time, or Relaxation time (1/2 $\pi f)$
Others	Marker continuous mode, $\Delta$ marker mode, Marker coupled mode, Marker value substitution (Marker $\rightarrow$ ), Marker zooming, Marker list, Marker statistics, Marker signal/dc bias monitor

#### **Equivalent Circuit Analysis**

Circuit model	3 component model (4 models), 4 component model (1 model)
Analysis type	Equivalent circuit parameters calculation, Frequency characteris- tics simulation

#### Limit Line Test

Available setup parameters for each	Sweep start value, sweep stop value, upper limit (middle value) segment and lower limit (delta limit) for sweep start, upper limit (middle value) and lower limit (delta limit) for sweep stop
Number of segments	1 to 18
Other functions	Beep fail, Limit line offset

#### **Mass Storage**

Flexible disk drive	
Туре	3.5 inch, Built-in
Size	1.44 MB
Format	DOS
Formatting	Available
Volatile memory disk	
Size	512 KB
Non-volatile memory disk (Flash memory)	
Size	10 MB
Stored data	State (binary), Data (binary or ASCII), Display graphics (TIFF)

#### **Printer Parallel Port**

Interface Standard	IEEE 1284 Centronics
Printer control language	HP PCL3 printer control language
Connector type	25 pin D-SUB connector

#### GPIB

Standard conformity	IEEE 448.1-1987, IEEE 488.2-1987, IEC 625, JIS C ,1901-1987
Available functions (function code) <sup>1</sup>	SH1, AH1, T6, TE0, L4, LE0, SR1, RL1, PP0, DC1, DT1, C1, C2, C3, C4, C11, E2
Numeric data transfer format	ASCII, 32 or 64 bit IEEE 754 floating point format, DOS PC format (32 bit IEEE reversed byte order)

1. See document of the standard for the meaning of each code.

#### Instrument BASIC

Keyboard	
Туре	PS/2 style 101 English
Connector Type	Mini-DIN connector

#### 8 Bit I/O Port

Connector type	15 pin D-SUB connector
Signal level	TTL
Number of I/O bit	4 bit for input, 8 bit for output
Pin assignment	(see Figure 7)

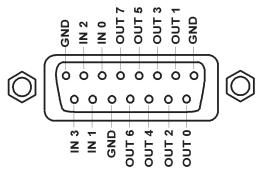


Figure 7. 8 Bit I/O Port Pin Assignment

#### 24 Bit I/O Port (Handler Interface)

Connector type	36 pin D-SUB connector
Signal level	TTL
Number of I/O bit	8 bit for input or output, 16 bit for output
Pin Assignment	(see Figure 8 and Table 1)



18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 56 35 34 33 32 31 30 29 28 27 26 25 24 23 22 21 20 19

 $\langle \rangle$ 

#### Figure 8. 24 Bit I/O Port Pin Assignment

#### Table 1. 24 Bit I/O Port Pin Assignment

Pin No.	Signal Name	Signal Standard
1	GND	0 V
2	INPUT1	TTL level, pulse input, pulse width: 1 $\mu s$ or above
3	OUTPUT1	TTL level, latch output
4	OUTPUT2	TTL level, latch output
5	Output port A0	TTL level, latch output
6	Output port A1	TTL level, latch output
7	Output port A2	TTL level, latch output
8	Output port A3	TTL level, latch output
9	Output port A4	TTL level, latch output
10	Output port A5	TTL level, latch output
11	Output port A6	TTL level, latch output
12	Output port A7	TTL level, latch output
13	Output port B0	TTL level, latch output
14	Output port B1	TTL level, latch output
15	Output port B2	TTL level, latch output
16	Output port B3	TTL level, latch output

#### Table 1. 24 Bit I/O Port Pin Assignment

Pin No.	Signal Name	Signal Standard
17	Output port B4	TTL level, latch output
18	Output port B5	TTL level, latch output
19	Output port B6	TTL level, latch output
20	Output port B7	TTL level, latch output
21	Input/Output port CO	TTL level, latch output
22	Input/Output port C1	TTL level, latch output
23	Input/Output port C2	TTL level, latch output
24	Input/Output port C3	TTL level, latch output
25	Input/Output port D0	TTL level, latch output
26	Input/Output port D1	TTL level, latch output
27	Input/Output port D2	TTL level, latch output
28	Input/Output port D3	TTL level, latch output
29	Port C status	TTL level, input mode; LOW, output mode: HIGH
30	Port D status	TTL level, input mode; LOW, output mode: HIGH
31	Write strobe signal	TTL level, active low, pulse output (width: 10 $\mu s,$ typical)
32	+5 V pullup	
33	SWEEP END signal	TTL level, active low, pulse output (width: 20 $\mu s,$ typical)
34	+5 V	+5 V, 100 mA MAX
35	PASS/FAIL signal	TTL level, PASS: HIGH, FAIL; LOW, latch output
36	PASS/FAIL write strobe signal	TTL level, active low, pulse output (width: 10 $\mu s,$ typical)

#### LAN Interface

Standard conformity	10 Base-T Ethertwist, RJ45 connector
Protocol	TCP/IP
Supported application	Telnet, FTP, FTP with automatic execution

### **General Characteristics**

#### **External Reference Input**

Frequency	10 MHz ±10 ppm (typical)
Level	– 5 dBm to +5 dBm (typical)
Input impedance	50 Ω (nominal)
Connector type	BNC (female)

### Internal Reference Output

Frequency	10 MHz (nominal)
Level	0 dBm (typical)
Output impedance	50 $\Omega$ (nominal)
Connector type	BNC (female)

#### High Stability Frequency Reference Output (Option 4294A-1D5)

Frequency	10 MHz (nominal)
Level	0 dBm (typical)
Output impedance	50 $\Omega$ (nominal)
Connector type	BNC (female)

#### **External Trigger Input**

Level	TTL
Pulse width (Tp)	$\geq 2~\mu s$ (typical); see Figure 9 for the definition of Tp.
Polarity	Positive or Negative (selective)
Connector type	BNC (female)

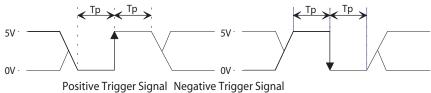


Figure 9. Required Pulse Width (Tp) for External Trigger Input

### External Program RUN/CONT Input

Level	ΠL	
Connector type	BNC (female)	
External Monitor Output		
Connector type	D-SUB, 15 pin HD	
Display resolution	640 X 480 (VGA)	
Operating Conditions		
Temperature		
Disk drive non-operating condition	0 °C to 40 °C	
Disk drive operating condition	10 °C to 40 °C	
Humidity (at wet bulb temperature ≤29 °C, without condensation)		
Disk drive non-operating condition	15% to 95% RH	
Disk drive operating condition	15% to 80% RH	
Altitude	0 m to 2,000 m	
Warm-up time	30 minutes	

#### **Non-operating Conditions**

Temperature	-20 °C to +60 °C
Humidity (at wet bulb temperature ≤45 °C, without condensation)	15% to 95% RH
Altitude	0 m to 4,572 m
Others	
EMC	EN 55011(1991)/CISPR 11(1990) Group 1, Class A EN 50082-1(1992)/IEC 61000-4-2(1995) 4 kV CD, 8 kV AD EN 50082-1(1992)/IEC 61000-4-3(1995) 3 V/m, 27 MHz to 1 GHz EN 50082-1(1992)/IEC 61000-4-4(1995) 0,5 kV Signal Line, 1 kV Power Line EN 61000-3-2(1995)/IEC 61000-3-2(1995) EN 61000-3-3(1995)/IEC 61000-3-3(1994)z
Safety	EN 61010-1(1993) +Amd2(1995)/IEC61010-1(1990) +Am1(1992) +Am2(1995) CSA-C22.2 N0.1010.1-92
Power requirement	90 V to 132 V, or 198 V to 264 V (automatically switched), 47 Hz to 63 Hz, 300 VA max.
Weight	25 kg (SPC)
Dimensions	See Figures 10 through 12.

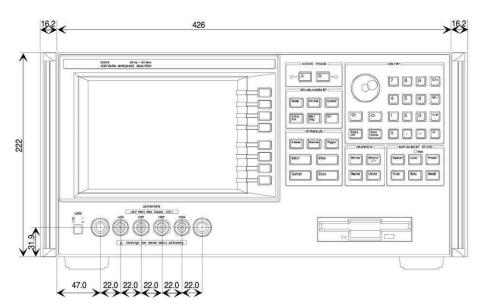


Figure 10. Agilent 4294A dimensions (front view, with Option 4294A-1CN/4294A-1D5, typical, in millimeters)

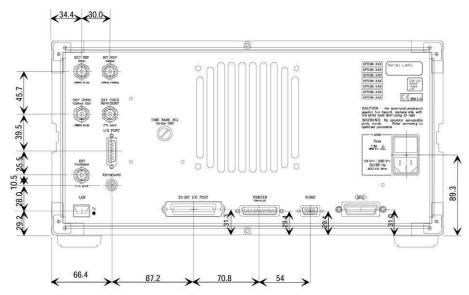


Figure 11. Agilent 4294A dimensions (rear view, with Option 4294A-1CN/4294A-1D5, typical, in millimeters)

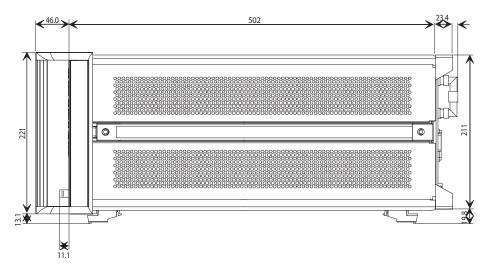


Figure 12. Agilent 4294A dimensions (side view, with Option 4294A-1CN/4294A-1D5, typical, in millimeters)

<sup>30</sup> www.valuetronics.com

### **Furnished Accessories**

Agilent Part Number	Description	Qty
04294-900X0/04294-970X0	Operation Manual (English/Japanese)	1
04294-900X1/04294-970X1	Programming Manual (English/Japanese) <sup>1</sup>	1
E2083-90005	Instrument BASIC User's Handbook <sup>1</sup>	1
04294-901X0	Service Manual <sup>2</sup>	1
04294-180X0	Sample Program Disk (3.5 inch) <sup>1</sup>	1
04294-61001	100 $\Omega$ Resister	1
	Mini-DIN Keyboard <sup>3</sup>	1
	Power Cable <sup>4</sup>	1
1250-1859	BNC Adapter <sup>5</sup>	1
5063-9229	Handle Kit <sup>6</sup>	1
5063-9216	Rackmount Kit <sup>7</sup>	1
5063-9223	Rackmount & Handle Kit <sup>®</sup>	1

Not furnished if Option 4294A-ABA/ABJ (English/Japanese manual) is not designated.
 Option 4294A-0BW (Add service manual) only.
 Not furnished if Option 4294A-810 (add keyboard) is not designated.
 The power cable depends on which country the instrument is used in.
 Option 4294A-1D5 (high stability frequency reference) only.
 Option 4294A-1CN (handle kit) only.
 Option 4294A-1CM (rackmount kit) only.
 Option 4294A-1CP (rackmount and handle kit) only.

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